ABSTRACT

Adaptive Clock Recovery

A method of recovering a clock signal for a TDM output from packets of TDM data which have been transmitted over a packet network, comprises

providing a packet buffer to store incoming packets after transmission over the packet network,

maintaining a packet count which is incremented as packets arrive at the packet buffer, and decremented each time a packet leaves the packet buffer, and

sampling the packet count and controlling the clock frequency of the TDM output on the basis of the sampled packet count.

Refer to Figure 2